

Amendments to the Specification:

Please amend the paragraph beginning at line 18 of page 8 and ending at line 3 of page 9 as follows:

While an embodiment of the present invention is shown and described, it is envisioned that those skilled in the art may devise various modifications of the present invention without departing from the spirit and scope of the appended claims. For example, while Figures 3 and 4 relate to the ~~situation~~ situation wherein the incrementor has eight output lines and the decoder has eight address inputs and 256 output lines, this is not imperative, and other sized incrementors and decoders can be used. In fact, while Figure 2 illustrates a logic circuit which includes an incrementer and decoder, other types of logic circuits can be employed in practicing the present invention.